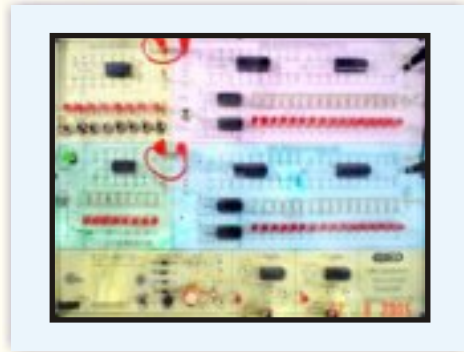




LONGITUDINAL REDUNDANCY CHECK (LRC CODING & DECODING)

MODEL - LRC100

This trainer has been designed with a view to provide practical and experimental knowledge of Longitudinal Redundancy Check Coding Decoding.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Built in IC based power supply.
3. 8 bit Digital Data generator to generate any binary input word
4. 8 bit Digital Data receiver to receive data
5. Sections : LRC (Longitudinal Redundancy Check) coding and decoding
6. Data bits : 8 bits
7. LRC Polynomials : 16 bits
8. All parts are soldered on single PCB with complete circuit diagram screen-printed in multicolor.
9. Standard Accessories :
 1. A Training Manual
 2. Connecting Patch cords.

EXPERIMENTS

1. To study theory of LRC Coding / Decoding.
2. To generate LRC signal.
3. To decode LRC signal.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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