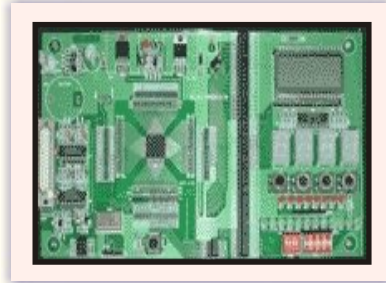




FPGA TRAINING KIT

MODEL FPGA100

This trainer has been designed with a view to provide practical and experimental Knowledge of FPGA Training technique on a SINGLE P.C.B.



SIGMA FPGA Kit is an excellent platform for learning VLSI Design. It provides an "all-in-one" platform that contains everything needed to implement a wide variety of circuits, making it the perfect choice for those encountering digital circuits, FPGAs and/or CAD tools for the first time. The FPGA used in the kit is fitted in 84-pin PLC socket. The kit comes with a Display I/O board and, one can apply 16 inputs and 16 outputs from it. The levels are displayed on LEDs. Additionally there are two seven-segment displays, which can be used to display counter functions. A J-Tag cable is provided with the kit. The bit stream can be downloaded through the cable. Alternately a copy of the bit stream can also be programmed in a serial EPROM and used to configure the FPGA. There is also a facility of parallel EPROM. The users can also use devices that are pin compatible to the, and use the parallel EPROM to configure them.

SPECIFICATIONS

1. Supports Xilinx FPGAs in PC84 package
2. (XC5202 / XC4000 / XCS Series etc)
3. On board 4 MHz Clock module Internal / External clock selection facility
4. Program Mode Selection
5. On Board Hardware Debugger port for programming
6. Program can be downloaded through EPROM or OTP
7. Power on Program with switch
8. PLD I/O pins available on connectors for external interface
9. The I/O Display Board within the kit has
 - Two 7- segment displays (non-multiplexed)
 - 16 Red LEDs for Output indication
 - 16 Green LEDs for Input indication
 - 16 DIP switches for Input stimulus
 - Two tactile switches (Push Button) for normal pulse operation

KIT INCLUDES

1. J-TAG Programming cable
2. + 5 V Power Supply (SMPS)
3. User Manual

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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