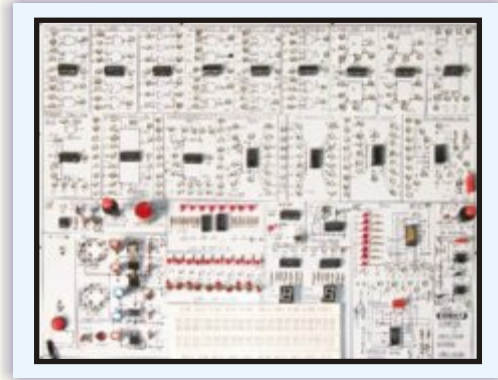




DIGITAL I.C. TRAINER-CMOS

MODEL DICDM102

This trainer has been designed with a view to provide practical and experimental knowledge of different digital IC.



SPECIFICATIONS

1. TTLGates
2. FFs
3. Counter
4. Master Slave J.K. FF,
5. Logic Gates Trainer
6. Modulo- N Counter Trainer
7. 4-Bit Ring Counter Trainer
8. 8-To-1 Line Multiplexer
9. Demultiplexer Trainer
10. 8-Bit ADC (Successive Approx Method) Trainer
11. 8-Bit DAC (Binary weighted Method) Trainer

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-