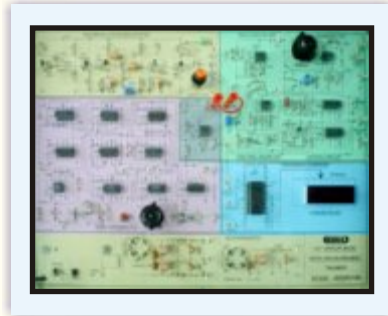




DATA CLOCK REGENERATION (FROM NRZ AND BI-PHASE DATA) TRAINER

MODEL - DCKGEN100

This trainer has been designed with a view to provide practical & experimental knowledge of the process of data clock regeneration from received data stream in digital system on single PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Built in IC based power supply.
3. On Board AF Modulating signal generator - Sinewave
Frequency Range : 20 Hz to 20 KHz in 3 ranges
Amplitude : 0 to 10 Vpp.
4. On Board Input Audio amplifier with Volume control for modulating external signal from Mike (Speech signal) or Tape recorder.
5. On Board Output Audio amplifier with speaker & Volume Control.
6. All parts are soldered on single PCB with complete circuit Diagram screenprinted.
7. Standard Accessories :
 1. A Training Manual
 2. Connecting Patch cords.

EXPERIMENTS

1. To understand Block diagram and working principle
2. To study theory of digital transmission.
3. To regenerate bit clock.
4. To synchronize the word clock.
5. To observe the effect of asynchronism on analog waveforms.
6. To understand - S/H - Integrate & Dump process.
7. To transmit Voice signal through digital channel and reconstruct at receiver

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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