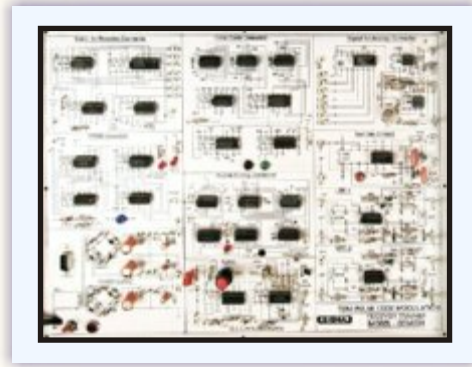




PULSE CODE DEMODULATION RECEIVER TRAINER MODEL-COM204

This trainer has been designed with a view to provide practical and experimental knowledge of Pulse Code Demodulation technique as practically implemented in Digital Communication systems on a SINGLE P.C.B.



SPECIFICATIONS

1. Input Channels : 2 TDM serial Input
2. Demodulation : Pulse code Demodulation
3. Clock Regeneration : by PLL
4. Low Pass Filter : Butter worth 4th order 3.4 KHz cutoff frequency (2 nos.)
5. Operating Speed : Fast 240KHz/channel (approx.)
Slow 1Hz/Channel (approx.)
6. Error Correction : Off-Odd-Even Parity Hamming Code
7. Test points : More Than 40
8. Interconnections : 4mm Sockets
9. PC-PC communication : 2 channels (RS 232)
10. Port : 9 pin D-type connector 2 nos.
11. Baud Rate : Selectable from 300 to 2400
12. Power : 220V $\pm 10\%$, 50/60 Hz, 4VA (approx.)
13. Accessories : Manual, Set of patch cord, Line cord, RS 232 cables -2 nos.

EXPERIMENTS

1. PCM Demodulation technique
2. Time Division De multiplexing of PCM data
3. Clock Regeneration by PLL
4. Effect of induced faults in the transmitter & receiver
5. Signal recovery in 3 connecting modes between transmitter & receiver
6. Clock & Frame synchronization in PCM system PC-PC communication in 3 modes

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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Dealer:-