



BINARY PHASE SHIFT KEYING MODULATION/ DEMODULATION TRAINER

MODEL - BPSK100

This trainer has been designed with a view to provide practical / experimental knowledge of BPSK. Modulation/Demodulation technique as practically implemented in Digital Communication systems on a single PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Built in IC based power supply.
3. On Board Modulating Digital Data signal generator to generate any binary input word with Bit clock & Word clock.
Word Length : 8 Bits.
Word Clock Frequency : 8 KHz to 12 KHz.
Data Format : NRZ & Bipolar NRZ
4. Modulator Type : Balanced Modulator as Phase modulator.
5. Demodulator Type : Balanced Modulator as Phase detector.
6. All parts are soldered on single pin TAGS on single PCB with complete circuit diagram Screen-printed.
7. Standard Accessories :
 1. A Training Manual.
 2. Connecting Patch cords.

EXPERIMENTS

1. To study theory of BPSK Modulation & Demodulation.
2. To generate BPSK signal
3. To demodulate BPSK signal using balanced modulators.
4. To observe BPSK modulated output by for different input binary codes.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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